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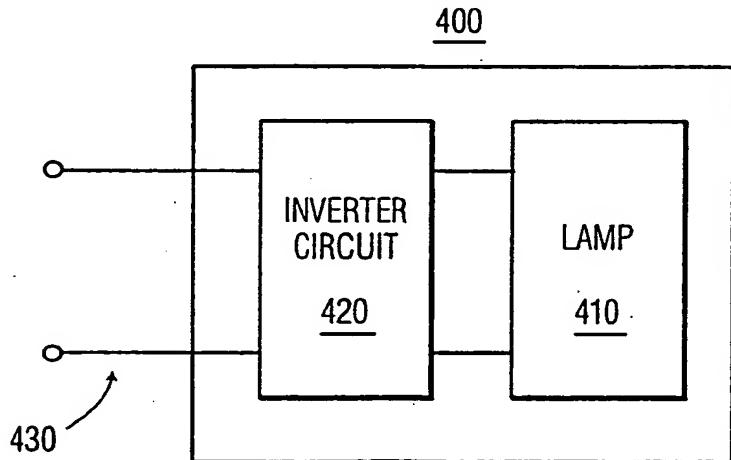
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(54) Title: SYSTEM FOR DRIVING A CAPACITIVELY COUPLED FLUORESCENT LAMP



(57) Abstract: A capacitively coupled fluorescent lamp package having a capacitively coupled fluorescent lamp; an inverter circuit for driving the lamp; and supply nodes for receiving a supply voltage is disclosed. The capacitively coupled fluorescent lamp package includes a resonant circuit lamp driving scheme for driving the capacitively coupled fluorescent lamp. The driving scheme reduces parasitic capacitance leakage current; compensates the reactive power using the secondary side leakage inductance in order to have the resonant frequency approximately equal the inverter circuit operating frequency for current source-type driven circuits; and forms a series resonant sub-circuit with the

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embedded ballasting capacitor and the secondary side leakage inductance for voltage source-type driven circuits, such that the resonant frequency is substantially less than the inverter circuit operating frequency and that the lamp current is properly shaped along with current ballasting.

## SYSTEM FOR DRIVING A CAPACITIVELY COUPLED FLUORESCENT LAMP

The present disclosure relates generally to lighting systems. More specifically, the present disclosure relates to a method and system for driving a capacitively coupled fluorescent lamp.

5 Capacitively coupled fluorescent lamps (CCFL) are widely used to backlight liquid crystal displays (LCD) and for other applications. Different electronic drivers or inverter circuits, for example, current-fed push-pull, voltage-fed push-pull, active clamped Flyback, and voltage-fed half-bridge inverter circuits, have been designed to operate CCFL lamps in high operating frequencies. A typical frequency range is between 20 kHz and 100  
10 kHz. In this way a high frequency voltage is applied in a discharge space within a discharge vessel or tube of the CCFL forming a discharge.

To increase the illuminance of the CCFL, the gas pressure of the rare gas which fills the discharge vessel or tube is increased. After increasing the gas pressure of the rare gas, the current required for discharge is not sufficient if the voltage applied to the CCFL  
15 and the high frequency of the voltage are not increased. Therefore, in order to increase the illuminance or lamp power of the CCFL, not only must the gas pressure of the rare gas be increased, but also the voltage and current applied to the CCFL. However, when the applied voltage is increased, there is the danger of discharge creeping on the outer surface of the discharge vessel which can lead to an insulation breakdown of the CCFL.

20 To overcome the disadvantages of conventional CCFLs, a capacitively coupled fluorescent lamp has been designed where the traditional cathodes (composed of two relatively heavy nickel-plated iron rectangular tabs forming a  $V$ ) are replaced by cylindrical ceramic tubes or capacitive coupling structures. Typically, the cylindrical ceramic tubes have an inner diameter of 2.5 mm, an outer diameter of 3.5 mm and a length of  
25 10 mm. Such ceramic tubes with certain dielectric constant and geometry effectively form series capacitance with the positive column of the lamp. The capacitance is not dependent on frequency. With proper material selection and construction, such series capacitance could be designed for the benefit of the electronic driver.

Due to the improvement of the cathodes, the lamp current is increased dramatically, without having to increase the pressure of the filled gas and the voltage applied to the lamp. In fact, when compared to conventional CCFLs, to deliver the same lamp power, the voltage applied to the capacitively coupled fluorescent lamp is less than the 5 voltage applied to conventional CCFLs.

Further, as an effect, the equivalent lamp impedance is greatly reduced. For example, in a preferred design for the capacitively coupled fluorescent lamp, the lamp voltage is 450 V and the lamp current is 20 mA at 50 kHz. Hence, the lamp impedance is approximately 22.5 kOhm compared with approximately 115 Kohm for conventional CCFLs. 10 Therefore, the capacitively coupled fluorescent lamp overcomes the problems associated with the prior art and also offers several advantages over conventional CCFLs.

As indicated above, electronic drivers or inverter circuits are used to operate CCFLs. In the circuit of FIG. 1, the inverter circuit is of voltage-fed-half-bridge type structure with LC resonant tank. The resonant inductor is  $L_r$ . The resonant capacitor is 15 formed by the equivalent shield parasitic capacitance and the equivalent output interwinding capacitance of the transformer  $T_1$ . The CCFL is denoted by its equivalent resistance  $R_{lp}$ . The ballast circuit is controlled by an IC.

A typical set of the CCFL structure parameters are: lamp voltage  $V_{lamp} \approx 690V$ ; lamp current  $I_{lamp} \approx 6 mA$ ; and equivalent shield capacitance  $C_{eq} \approx 7pF$  as shown in FIG. 2. 20 Also, a set of circuit design values are: DC input voltage  $V_{in} \approx 12V$ ; resonant inductor  $L_r \approx 6.5uH$ ; and output transformer turns ratio  $N=200$ .

In the circuit shown by FIG. 1, it can be calculated that the lamp equivalent impedance is 115 kOhm in the nominal steady state. At 50 kHz operating frequency, the equivalent impedance of the shield capacitance is 454.7 kOhm. Such very comparable 25 impedance leads to large parasitic capacitance leakage current through the shield with respect to the lamp current. The circuit power losses are then increased. More importantly, it is difficult to design a unified electronic driver for different type of monitors without brutal forced lamp current feedback control.

In most of designed LCD backlight inverters, the inverter circuit output 30 impedance is limited by the circuit internal structure. To achieve lamp stability, the lamp should be properly ballasted. One ballasting scheme for the narrow diameter CCFL is by connecting a small capacitor  $C_b$  in series with the lamp as shown in FIG. 3.

The series capacitance as in FIG. 3 is naturally buried in the structure of the capacitively coupled fluorescent lamp. For the above described capacitively coupled

fluorescent lamp, by using the series capacitance, the equivalent capacitance  $C_b$  is in the range of 1.1 - 1.4 nF. At an operating frequency of 50 kHz, the corresponding equivalent impedance is in the range of 2.89 - 2.27 kOhm. It is measured that the dynamic negative impedance of the capacitively coupled fluorescent lamp is in the range of 5.0 - 7.0 kOhm in steady state with a frequency range of 25 - 100 kHz.

5 Due to the high AC voltage across the lamp (~500 V) and the low input DC voltage (~12 V), in most LCD monitor applications, a high voltage step up output transformer is inevitable. Such high turns ratio usually generates high leakage inductance on the secondary side of the output transformer, i.e., secondary side leakage inductance. When 10 the inverter circuit is connected to the capacitively coupled fluorescent lamp, such leakage inductance naturally forms a series resonant sub-circuit with the ballasting capacitor  $C_b$  which usually adversely affects the operation of the capacitively coupled fluorescent lamp.

15 The lead value of the ballasting capacitor  $C_b$  is depending on the lamp type and operating frequencies. In some commercial prototypes,  $C_b$  is typically in the range of 15 - 68 pF. The disadvantage of this implementation is that the output transformer usually needs to be over designed, such that the reactive power could be transferred to the lamp/ballasting 20 capacitor branch.

Accordingly, there is a need for providing a resonant circuit lamp driving scheme which depending on the inverter circuit type, is capable of choosing different 25 operating points for driving the capacitively coupled fluorescent lamp in order to reduce the transformer size, inverter circuit losses and improve the lamp current waveform.

In accordance with the present disclosure, a method and system are provided for driving a capacitively coupled fluorescent lamp which obviates the problems associated with the prior art.

25 The method and system of the present disclosure provide a resonant circuit lamp driving scheme for driving the capacitively coupled fluorescent lamp which reduces parasitic capacitance leakage current and consequently reduce the inverter circuit losses; compensates the reactive power using the secondary side leakage inductance to have the resonant frequency approximately equal the inverter circuit operating frequency for current 30 source-type driven circuits and consequently reduce the output transformer size, turns ratio and losses; and forms a series resonant sub-circuit with the embedded ballasting capacitor and the secondary side leakage inductance for voltage source-type driven circuits, such that the resonant frequency is substantially less than the inverter circuit operating frequency and

the lamp current is properly shaped along with current ballasting, and consequently reduce the turns ratio of the output transformer.

The disclosed system includes a capacitively coupled fluorescent lamp having cylindrical ceramic tubes. The system further includes an electronic driver or inverter circuit for driving the lamp and supply nodes for receiving a supply voltage. The inverter circuit is a conventional inverter circuit, such as, for example, current-fed push-pull, voltage-fed push-pull, active clamped Flyback, and voltage-fed half-bridge inverter circuits, used in conventional CCFLs.

Specifically, the present disclosure provides a capacitively coupled fluorescent lamp package having a capacitively coupled fluorescent lamp; an inverter circuit for driving the lamp; and supply nodes for receiving a supply voltage. A ballast circuit controlled by an integrated circuit may be connected to the inverter circuit for properly ballasting the lamp.

The capacitively coupled fluorescent lamp package includes a resonant circuit lamp driving scheme for driving the capacitively coupled fluorescent lamp. The driving scheme reduces parasitic capacitance leakage current; compensates the reactive power using the secondary side leakage inductance in order to have the resonant frequency approximately equal the inverter circuit operating frequency for current source-type driven circuits; and forms a series resonant sub-circuit with the embedded ballasting capacitor and the secondary side leakage inductance for voltage source-type driven circuits, such that the resonant frequency is substantially less than the inverter circuit operating frequency and the lamp current is properly shaped along with current ballasting.

Embodiments of the invention will be further discussed making reference to a drawing. In the drawing

FIG. 1 is a schematic diagram of a prior art inverter circuit;

FIG. 2 is an equivalent diagram of a lamp/shield connection of the inverter circuit of FIG. 1;

FIG. 3 is an equivalent diagram of a ballasting capacitor/lamp/shield connection of the inverter circuit of FIG. 1;

FIG. 4 is a block diagram of a capacitively coupled fluorescent lamp package having a lamp capable of being driven by a resonant circuit lamp driving scheme according to the present disclosure; and

FIG. 5 is a chart illustrating an operating frequency range for an inverter circuit of the capacitively coupled fluorescent lamp package according to the resonant circuit lamp driving scheme according to the present disclosure.

5 A preferred embodiment of the presently disclosed resonant circuit lamp driving scheme for driving a capacitively coupled fluorescent lamp within a capacitively coupled fluorescent lamp package will now be described in detail with reference to FIGS. 4 and 5. While the embodiment disclosed herein is designed for backlighting a liquid crystal display (LCD), the presently disclosed embodiment can be used in other applications.

10 With reference to FIG. 4, there is shown a block diagram of a capacitively coupled fluorescent lamp package utilizing the resonant circuit lamp driving scheme according to the present disclosure. The capacitively coupled fluorescent lamp package designated generally by reference numeral 400 includes a capacitively coupled fluorescent lamp 410 having a discharge vessel and cylindrical ceramic tubes. The lamp package 400 further includes an electronic driver or inverter circuit 420 for driving the lamp 410 and 15 supply nodes 430 for receiving a supply voltage from a voltage or power supply (not shown). The supply voltage is approximately 450 V.

20 → Preferably, the inverter circuit 420 supplies a 20 kHz and 100 kHz driving signal to the capacitively coupled fluorescent lamp 410. The 20 kHz driving signal is supplied, as described below, when the inverter circuit 420 is a current source driven type inverter circuit. The 100 kHz driving signal is supplied, as described below, when the inverter circuit 420 is a voltage source driven type inverter circuit.

25 The inverter circuit 420 is a conventional inverter circuit, such as, for example, current-fed push-pull, voltage-fed push-pull, active clamped Flyback, and voltage-fed half-bridge inverter circuits, used in conventional CCFLs. According to the present disclosure, depending on the inverter circuit type, a different operating point should be chosen in order to 30 reduce the transformer size, inverter circuit 420 losses and improve the lamp current waveform.

35 In current source driven type inverter circuits, the output impedance of the inverter circuit 420 is usually very high. Direct lamp driving is possible. In this case, the output transformer secondary side leakage inductance and the coupling capacitance of the 40 capacitively coupled fluorescent lamp 410 is designed to have the resonant frequency approximately equal the inverter circuit operating frequency.

45 In such a way, the coupling capacitance is completely compensated by the output transformer leakage inductance. In practice, even the resonant frequency does not

match the inverter circuit operating frequency; their difference is kept as small as possible. The resonant circuit lamp driving scheme provides for keeping the inverter circuit operating frequency around the valley of the curve in FIG. 5, where the input impedance of the secondary side series resonant sub-circuit formed by the leakage inductance and the ballasting capacitor  $C_b$  is shown. Preferably, the operating frequency is 20 kHz.

5 In the case of voltage source driven type inverter circuits, the output impedance of the inverter circuit 420 is usually not high. Direct lamp driving usually leads to circuit instability. In this case, the output transformer secondary side leakage inductance and the coupling capacitance in the capacitively coupled fluorescent lamp 410 serves as second-  
10 order resonant sub-circuit for ballasting the capacitively coupled fluorescent lamp 410, as well as shaping the lamp current.

15 The resonant circuit lamp driving scheme provides for keeping the resonant frequency substantially below the inverter circuit operating frequency, i.e., the resonant frequency is less than the inverter circuit operating frequency. That is, the inverter circuit operating frequency range is substantially away from the valley of the curve in FIG. 5. Preferably, the operating frequency is 100 kHz. In such a way, the coupling capacitance is partially compensated by the output transformer leakage inductance.

20 In backlighting an LCD, the lamp package 400 is installed within a system having the LCD, such as a laptop computer, and the supply nodes 430 are connected to the voltage or power supply for providing a supply voltage. The inverter circuit 420 is then powered by the supply voltage. Accordingly, the inverter circuit 420 transmits drive signals using the resonant circuit lamp driving scheme according to the present disclosure to the capacitively coupled fluorescent lamp 410 causing the lamp 410 to achieve luminance for backlighting the LCD.

25 It is contemplated to also provide a housing for fully enclosing the lamp 410, the inverter circuit 420 and partially enclosing the supply nodes 430. It is also contemplated to provide a ballast circuit for ballasting the capacitively coupled fluorescent lamp 410 and to provide an integrated circuit, as shown by FIG. 1, for controlling the ballast circuit.

30 Preferably, the inverter circuit 420 is selected from the group consisting of current-fed push-pull, voltage-fed push-pull, active clamped Flyback, and voltage-fed half-bridge inverter circuits.

It will be understood that various modifications may be made to the embodiments disclosed herein and that the above description should not be construed as

limiting, but merely as exemplifications of preferred embodiments. Those skilled in the art will envision other modifications within the scope and spirit of the claims appended hereto.

## CLAIMS:

1. A capacitively coupled fluorescent lamp package 400 comprising:
  - a fluorescent lamp 410 adapted for capacitive coupling to an inverter circuit;
  - an inverter circuit 420 configured for driving the fluorescent lamp 410 at an operating frequency which compensates for the coupling capacitance of the fluorescent lamp 410; and
  - 5 supply nodes 430 for receiving a supply voltage.
2. The lamp package according to Claim 1, wherein the inverter circuit 420 is configured for driving the lamp 410 at an approximate operating frequency of 20 kHz or 100 kHz.
- 10 3. The lamp package according to Claim 2, wherein the inverter circuit 420 drives the lamp 410 at the approximate operating frequency of 20 kHz if the inverter circuit 420 is a current source driven type inverter circuit and the inverter circuit 420 drives the lamp 15 at the approximate operating frequency of 100 kHz if the inverter circuit is a voltage source driven type inverter circuit.
4. The lamp package according to Claim 1, wherein the inverter circuit 420 is selected from the group consisting of current-fed push-pull, voltage-fed push-pull, active 20 clamped Flyback, voltage-fed half-bridge inverter circuits, and other high frequency inverters.
5. The lamp package according to Claim 1, wherein the capacitively coupled fluorescent lamp 410 includes a discharge vessel and cylindrical ceramic tubes within the 25 discharge vessel.
6. The lamp package according to Claim 5, wherein the cylindrical ceramic tubes 104 have an inner diameter of approximately 2.5 mm, an outer diameter of approximately 3.5 mm and a length of approximately 10 mm.

7. The lamp package according to Claim 1, wherein the supply voltage is approximately 450 V.

8. The lamp package according to Claim 1, wherein the capacitively coupled 5 fluorescent lamp 410 has a lamp current of approximately 20 mA and an operating frequency of approximately 50 kHz.

9. The lamp package according to Claim 1, wherein the capacitively coupled fluorescent lamp 410 has a lamp impedance of approximately 22.5 kOhm.

10. 10. The lamp package according to Claim 1, wherein the inverter circuit is part of a ballast circuit for ballasting the capacitively coupled fluorescent lamp 410.

11. 11. A capacitively coupled fluorescent lamp package 400 comprising:  
15 a fluorescent lamp 410 adapted for capacitive coupling to an inverter circuit;  
an inverter circuit 420 capacitively coupled to the fluorescent lamp 410 and configured for driving the capacitively coupled lamp 410 at an operating frequency  $f$ ; and  
supply nodes 430 for receiving a supply voltage,  
characterized in that the inverter comprises an inductive element L coupled in series with the  
20 fluorescent lamp and in that the inductive element L is dimensioned such that the resonance frequency  $f_r$  of the inductive element L and the coupling capacitance,

is chosen in range  $0.8*f \leq f_r \leq 1.2*f$ , preferably in the range  $0.9*f \leq f_r \leq 1.1*f$  in case the inverter is of the current source driven type, and

25 is chosen in the range  $f_r \leq 0.5*f$ , preferably  $f_r \leq 0.2*f$  in case the inverter is of the voltage source driven type.

12. 12. A capacitively coupled fluorescent lamp package according to claim 11, wherein the inverter comprises a transformer equipped with a primary winding and a secondary winding and wherein the inductive element L is formed by a secondary side 30 leakage inductance of the transformer.

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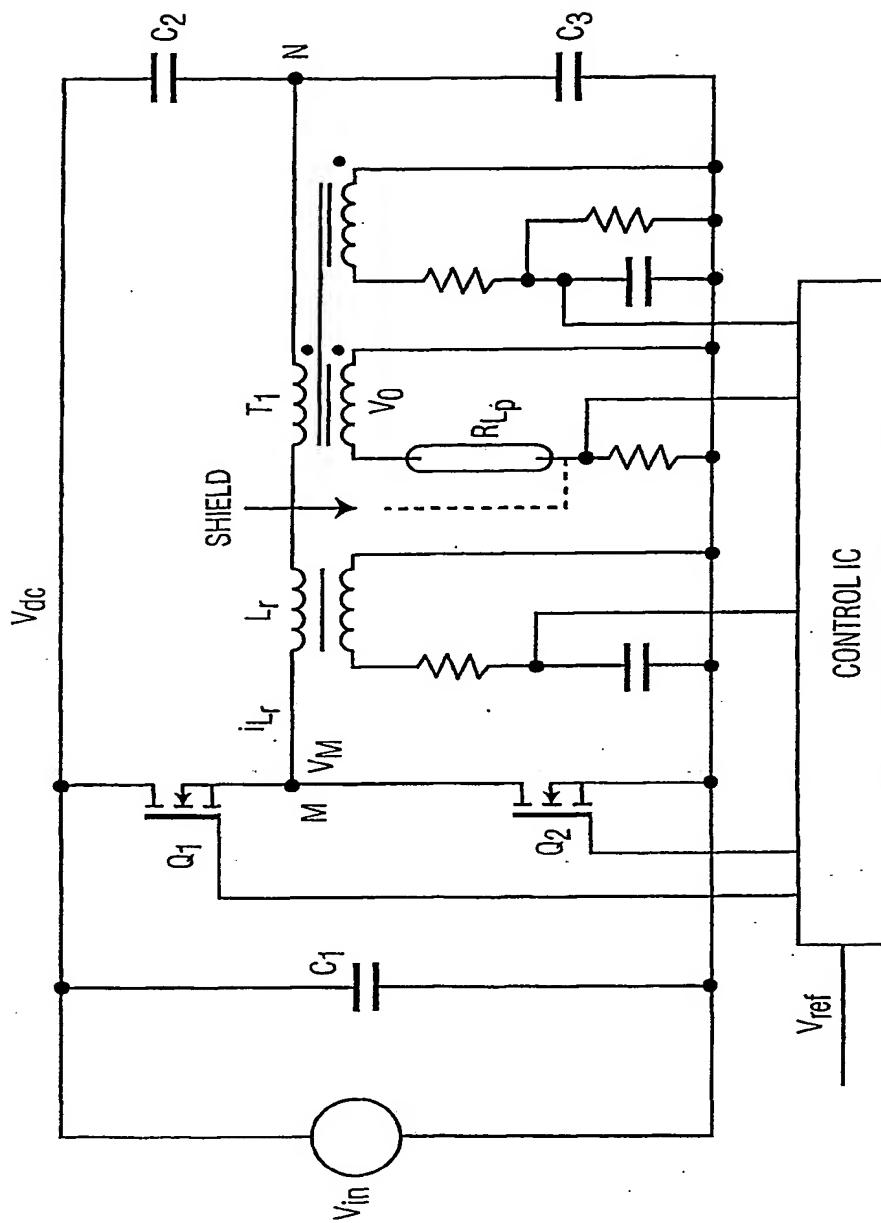


FIG. 1

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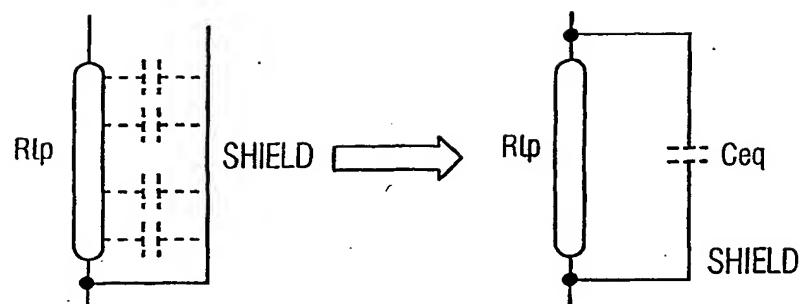


FIG. 2

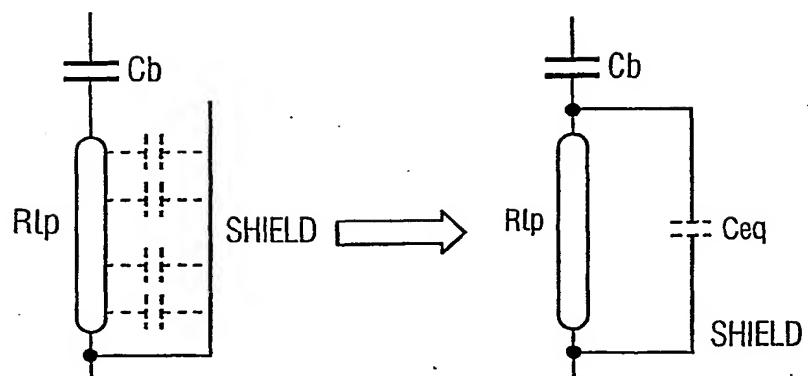


FIG. 3

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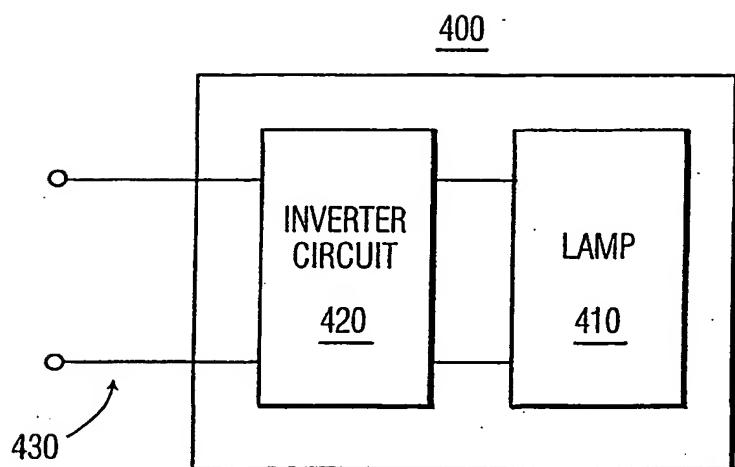


FIG. 4

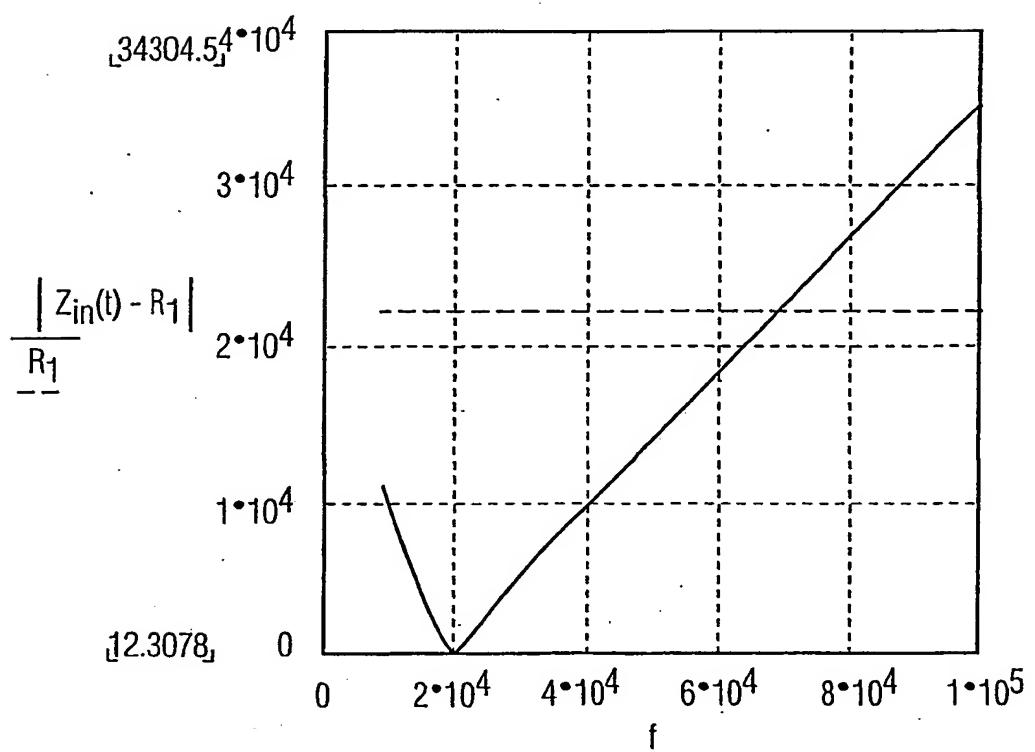


FIG. 5

## INTERNATIONAL SEARCH REPORT

International Application No

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A. CLASSIFICATION OF SUBJECT MATTER  
 IPC 7 H05B41/282 H05B41/288 H01J61/78

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H05B H01J

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the International search (name of data base and, where practical, search terms used)

EPO-Internal, INSPEC, IBM-TDB

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	<p>DONAHUE J A ET AL: "The LCC inverter as a cold cathode fluorescent lamp driver"            APPLIED POWER ELECTRONICS CONFERENCE AND EXPOSITION, 1994. APEC '94. CONFERENCE PROCEEDINGS 1994., NINTH ANNUAL ORLANDO, FL, USA 13-17 FEB. 1994, NEW YORK, NY, USA, IEEE,            13 February 1994 (1994-02-13), pages 427-433, XP010118539            ISBN: 0-7803-1456-5            page 427 -page 430; figures 1-7</p>	1,2,4,7, 10
X	<p>US 6 114 814 A (HSING MICHAEL REN ET AL)            5 September 2000 (2000-09-05)            column 7, line 23 -column 11, line 12;            figures 1-5</p> <p>---</p> <p>-/-</p>	1,2,4,7

Further documents are listed in the continuation of box C.

Patent family members are listed in annex.

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## INTERNATIONAL SEARCH REPORT

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PCT/IB 02/00718

## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	JORDAN M ET AL: "Resonant fluorescent lamp converter provides efficient and compact solution" PROCEEDINGS OF THE ANNUAL APPLIED POWER ELECTRONICS CONFERENCE AND EXPOSITION (APEC). SAN DIEGO, MAR. 7 - 11, 1993, NEW YORK, IEEE, US, vol. CONF. 8, 7 March 1993 (1993-03-07), pages 424-431, XP010111265 ISBN: 0-7803-0983-9 page 424 -page 429; figures 1-7 US 5 619 402 A (LIU KWANG H) 8 April 1997 (1997-04-08) column 4, line 14 -column 9, line 22; figures 1-4 EP 1 137 050 A (PHILIPS CORP INTELLECTUAL PTY ;KONINKL PHILIPS ELECTRONICS NV (NL)) 26 September 2001 (2001-09-26) abstract; figure 1 WO 01 20642 A (GL DISPLAYS INC) 22 March 2001 (2001-03-22) abstract; figures 1-14 US 5 381 073 A (GODYAK VALERY A ET AL) 10 January 1995 (1995-01-10) abstract; figures 5,6 US 5 930 121 A (HENRY GEORGE C) 27 July 1999 (1999-07-27) TRAVIS B: "LITTLE ICS GENERATE LONG VOLTAGES" EDN ELECTRICAL DESIGN NEWS, CAHNERS PUBLISHING CO. NEWTON, MASSACHUSETTS, US, vol. 13, no. 45, 22 June 2000 (2000-06-22), pages 73-74,76,78,80,82, XP001074433 ISSN: 0012-7515 REDL R ET AL: "A low-cost control IC for single-transistor ZVS cold-cathode fluorescent lamp inverters and DC/DC converters" APPLIED POWER ELECTRONICS CONFERENCE AND EXPOSITION, 1997. APEC '97 CONFERENCE PROCEEDINGS 1997., TWELFTH ANNUAL ATLANTA, GA, USA 23-27 FEB. 1997, NEW YORK, NY, USA, IEEE, US, 23 February 1997 (1997-02-23), pages 1042-1049, XP010215787 ISBN: 0-7803-3704-2	1-4,7, 10-12 1-4,7, 10-12 1,5,6,11 1,5,6,11 1,11 -/-

## INTERNATIONAL SEARCH REPORT

International Application No

PCT/IB 02/00718

## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	<p>NALBANT M K: "A new and improved control technique greatly simplifies the design of ZVS resonant inverters and DC/DC power supplies" APPLIED POWER ELECTRONICS CONFERENCE AND EXPOSITION, 1995. APEC '95. CONFERENCE PROCEEDINGS 1995., TENTH ANNUAL DALLAS, TX, USA 5-9 MARCH 1995, NEW YORK, NY, USA, IEEE, US, 5 March 1995 (1995-03-05), pages 694-701, XP010147721 ISBN: 0-7803-2482-X</p> <p>—</p>	
A	<p>LIN MU-SHEN ET AL: "Primary-side dimming control driver for cold-cathode fluorescent lamps" ELECTRONICS LETTERS, IEE STEVENAGE, GB, vol. 32, no. 15, 18 July 1996 (1996-07-18), pages 1334-1335, XP006005412 ISSN: 0013-5194</p> <p>—</p>	

## INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/IB 02/00718

Patent document cited in search report		Publication date		Patent family member(s)		Publication date
US 6114814	A	05-09-2000	US	6316881 B1		13-11-2001
			US	2002047601 A1		25-04-2002
US 5619402	A	08-04-1997	CA	2200932 A1		16-10-1997
			DE	69702998 D1		12-10-2000
			DE	69702998 T2		12-04-2001
			EP	0802702 A2		22-10-1997
			JP	10050489 A		20-02-1998
EP 1137050	A	26-09-2001	DE	10014407 A1		27-09-2001
			CN	1319876 A		31-10-2001
			EP	1137050 A1		26-09-2001
			JP	2001291492 A		19-10-2001
			US	2001024090 A1		27-09-2001
WO 0120642	A	22-03-2001	CN	2385435 U		28-06-2000
			CN	2394323 U		30-08-2000
			CN	2411569 U		20-12-2000
			CN	1290951 A		11-04-2001
			CN	2457735 U		31-10-2001
			CN	2433730 U		06-06-2001
			US	6337543 B1		08-01-2002
			AU	7367100 A		17-04-2001
			WO	0120642 A1		22-03-2001
			CN	1326216 A		12-12-2001
US 5381073	A	10-01-1995	US	5300860 A		05-04-1994
			DE	69322731 D1		04-02-1999
			DE	69322731 T2		29-07-1999
			EP	0593312 A2		20-04-1994
US 5930121	A	27-07-1999	US	5923129 A		13-07-1999

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